

# APPROVAL SHEET

MULTILAYER CERAMIC CAPACITORS

Microwave MLCC with Narrow-Tolerance (UF)

0201 & 0402 Sizes (25V to 100V)

NP0 Dielectric

Halogen Free & RoHS Compliance

\*Contents in this sheet are subject to change without prior notice.

#### 1. INTRODUCTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

WTC UF series MLCC is used at high frequencies generally have a small temperature coefficient of capacitance, typical within the ±30ppm/°C required for NP0 (C0G) classification, Ultra-narrow tolerance of capacitance and have excellent conductivity internal electrode. Thus, WTC UF series MLCC will be with the feature of low ESR and high Q characteristics.

#### 2. FEATURES

- a. High Q and low ESR performance at high frequency.
- b. Ultra low capacitance to 0.05pF.
- c. Can offer ultra-narrow tolerance to ±0.02pF.
- d. Quality improvement of telephone calls for low power loss and better performance.

#### 3. APPLICATIONS

- a. Telecommunication products & equipments: Mobile phone, WLAN, Base station.
- b. RF module: Power amplifier, VCO.
- c. Tuners.

#### 4. HOW TO ORDER

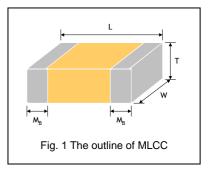
<u>UF</u>	<u>15</u>	<u>N</u>	<u>R05</u>	<u>P</u>	<u>250</u>	<u>C</u>	I
<u>Series</u>	<u>Size</u>	<u>Dielectric</u>	<u>Capacitance</u>	Tolerance	Rated voltage	<u>Termination</u>	<u>Packaging</u>
UF= Microwave MLCC with narrow-tolerance	<b>03</b> =0201 (0603) <b>15</b> =0402 (1005)	N=NPO	Two significant digits followed by no. of zeros. And R is in place of decimal point.	<b>A</b> =±0.05pFN <b>B</b> =±0.1pF	Two significant digits followed by no. of zeros. And R is in place of decimal point.	<b>C</b> =Cu/Ni/Sn	T=7" reeled G= 13" reeled
			eg.: R05=0.05pF 0R5=0.5pF 1R0=1.0pF	Cap. ≥10pF F=±1% G=±2%	250=25 VDC 500=50 VDC 101=100 VDC		



#### **5. EXTERNAL DIMENSIONS**

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Sym	bol	Remark	M <sub>B</sub> (mm)
0201 (0603)	0.60±0.03	0.30±0.03	0.30±0.03	L	#	0.15±0.05
0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	#	0.25+0.05/-0.10

<sup>#</sup> Reflow soldering only is recommended.



#### **6. GENERAL ELECTRICAL DATA**

Dielectric	NP0
Size	0201, 0402
Capacitance*	0.05pF to 10pF
Capacitance tolerance	P (±0.02pF), Q (±0.03pF), A (±0.05pF), B (±0.1pF), Cap≥10pF: F (±1%), G (±2%)
Rated voltage (WVDC)	25V, 50V, 100V
Q**	Q≥400+20C
Insulation resistance at Ur	≥10GΩ or RxC≥100Ω-F whichever is smaller.
Operating temperature	-55 to +125°C\ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Capacitance change	±30ppm/°C
Termination	Ni/Sn (lead-free termination)

<sup>\*\*</sup> Measured at the conditions of 25℃ ambient tempe rature and 30~70% related humidity. Apply 1.0±0.2Vrms, 1.0MHz±10%

#### 7. PACKAGING DIMENSION AND QUANTITY

Size	Thickness (mm)/Symbol	Pape	Paper tape	
Size	Thickness (min)/Symbol	7" reel	13" reel	
0201 (0603)	0.30±0.03	15,000	70,000	
0402 (1005)	0.50±0.05	10,000	50,000	

PASSIVE SYSTEM ALLIANCE

Unit: pieces



#### Approval Sheet

## **8. CAPACITANCE RANGE**

DIELECTRIC				NP	0		
SIZE		0201			040		
RAT	TED VOLTAGE (VDC)	25	50	100	25	50	Tolerance
	0.05pF (R05)	L	L		N	N	P, Q, A
	0.1pF (0R1)	L	L	L*	N	N	P, Q, A
	0.2pF (0R2)	L	L	L*	N	N	P, Q, A
	0.3pF (0R3)	L	L	L*	N	N	P, Q, A
	0.4pF (0R4)	L	L	L*	N	N	P, Q, A
	0.5pF (0R5)	L	L	L*	N	N	P, Q, A
	0.6pF (0R6)	L	L	L*	N	N	P, Q, A
	0.7pF (0R7)	L	L	L*	N	N	P, Q, A
	0.8pF (0R8)	L	L	L*	N	N N	P, Q, A
	0.9pF (0R9)	L .	L	L*	N	N N	P, Q, A
	1.0pF (1R0)	L	L	L*	N	N	P, Q, A
	1.1pF (1R1) 1.2pF (1R2)	L	L	L	N N	N N	A, B
	1.3pF (1R3)	L	L	L	N	N N	A, B
	1.5pF (1R5)	L L	L	L	N	N N	A, B
	1.6pF (1R6)	L	L	L	N	N N	A, B A, B
	1.8pF (1R8)	L	L	L	N	N	A, B
	2.0pF (2R0)	L	L	<u> </u>	N	N	A, B
	2.2pF (2R2)	L	L	2. 右	N	N	A, B
o)	2.4pF (2R4)	L	F. 15.	所是	J. SN	N	A, B
Capacitance	2.7pF (2R7)		L	. L IT 1/	N	N	A, B
cita	3.0pF (3R0)	L	//K/A	汉区历	N	N	A, B
aba	3.3pF (3R3)	L	15Vm LLT	L	1/20 (1/4)		A, B
ပိ	3.6pF (3R6)	L	/ W// LAN	L	(-) So		A, B
	3.9pF (3R9)	L	11/1	L	711		A, B
	4.0pF (4R0)	L	Ľ				A, B
	4.3pF (4R3)	L	L	L CYCLE	ALL TANGE		A, B
	4.7pF (4R7)	L	84	L	2 5	3	A, B
	5.0pF (5R0)	L	盖 5	L	28		A, B
	5.1pF (5R1)	L	Se Lo	L	.6	/	A, B
	5.6pF (5R6)	L	(0)-2>	L	(0)		A, B
	6.0pF (6R0)	L	139	Ch L	(0) (0)		A, B
	6.2pF (6R2)	L	10/1/2	ringlog			A, B
	6.7pF (6R7)	L	1	WOLOEY COR	OBUTON		A, B
	6.8pF (6R8)	L	L		OT I		A, B
	7.0pF (7R0)	L	L	L			A, B
	7.5pF (7R5) 8.0pF (8R0)	L L	L	L			A, B A, B
			_				
	8.2pF (8R2) 9.0pF (9R0) 9.1pF (9R1) 9.9pF (9R9) 10pF (100)	L L L L	L L L	L L L			A, B A, B A, B A, B F,G

- 1. The letter in cell is expressed the symbol of product thickness.
- 2. The letter in cell with "\*" mark is expressed product not in "P"&"Q" tolerance; "A" tolerance only.
- 3. For more information about products with special capacitance or other data, please contact WTC local representative.

#### **Multilayer Ceramic Capacitors**

#### Approval Sheet

### 9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Conditions	Requirements
1.	Visual and		* No remarkable defect.
	Mechanical		* Dimensions to conform to individual specification sheet.
2.	Capacitance	* Test temp.: Room Temperature.	* Shall not exceed the limits given in the detailed spec.
3.	Q/ D.F.	1.0±0.2Vrms, 1MHz±10%	* Cap.=0.05pF: Q≥300
	(Dissipation		* 0.1pF≤Cap.<30pF: Q≥400+20C
	Factor)		
4.	Dielectric	*To apply voltage: 250% of rated voltage.	* No evidence of damage or flash over during test.
	Strength	*Duration: 1 to 5 sec.	
		*Charge & discharge current less than 50mA.	
5.	Insulation	* Test temp.: Room Temperature.	≥10GΩ or RxC≥100Ω-F whichever is smaller
	Resistance	* To apply rated voltage for max. 120 sec.	
6.	Temperature	With no electrical load.	* Capacitance change: within ±30ppm/℃;
	Coefficient	Operating temperature: -55~125℃ at 25℃	
7.	Adhesive	* Pressurizing force :	* No remarkable damage or removal of the terminations.
	Strength of	0201: 2N	
	Termination	0402: 5N	The Z
		* Test time: 10±1 sec.	
8.	Vibration	* Vibration frequency: 10~55 Hz/min.	* No remarkable damage.
	Resistance	* Total amplitude: 1.5mm	* Cap change and Q/D.F.: To meet initial spec.
		* Test time: 6 hrs. (Two hrs each in three mutually	50
		perpendicular directions.)	TILL
		*Cap./DF(Q) Measurement to be made after de-aging at 150℃	
	0.111224	for 1hr then set for 24±2 hrs at room temp.	TANCE
9.	Solderability	* Solder temperature: 235±5°C	95% min. coverage of all metalized area.
10	Bending Test	* Dipping time: 2±0.5 sec.	* No remarkable demage
10.	bending lest	* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until	* No remarkable damage.  * Cap change: within ±5.0% or ±0.5pF whichever is larger.
		the deflection becomes 1 mm and then the pressure shall be	(This capacitance change means the change of capacitance under
		maintained for 5±1 sec.	specified flexure of substrate from the capacitance measured before
		* Measurement to be made after keeping at room temp. for	the test.)
		24±2 hrs.	
11.	Resistance to	* Solder temperature: 260±5℃	* No remarkable damage.
	Soldering Heat		* Cap change: within ±2.5% or ±0.25pF whichever is larger.
		* Preheating: 120 to 150°C for 1 minute before imme rse the	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.
		capacitor in a eutectic solder.	* 25% max. leaching on each edge.
		*Cap. / DF(Q) / I.R. Measurement to be made after de-aging at	
		150℃ for 1hr then set for 24±2 hrs at room temp.	

<sup>\* &</sup>quot;Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

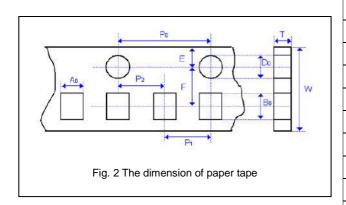
#### **Multilayer Ceramic Capacitors**

No.	Item	Test Condition			Requir	rements	
12.	Temperature	* Conduc	t the five cycles according to the te	mperatures and	No remarkable damage.		
	Cycle	time.		* Cap change: within ±2.5% or ±0.25pF whichever is larger.			
		Step	Temp. (℃)	Time (min.)	* Q/D.F., I.R. and dielectric stren	gth: To meet initial requirements.	
		1	Min. operating temp. +0/-3	30±3			
		2	Room temp.	2~3			
		3	Max. operating temp. +3/-0	30±3			
		4	Room temp.	2~3			
		* Cap. / C	OF(Q) / I.R. Measurement to be made	de after de-aging at			
		150℃ for	1hr then set for 24±2 hrs at room t	emp.			
13.	Humidity	* Test ten	np.: 40±2℃		* No remarkable damage		
	(Damp Heat)	* Humidit	y: 90~95% RH		* No remarkable damage.	O. Fo Freshiph accessing largers	
	Steady State	* Test tim	e: 500+24/-0hrs.		* Cap change: within ±5.0% or ±	:0.5pF whichever is larger.	
		* Cap. / D	DF(Q) / I.R. Measurement to be made	de after de-aging at	* Q/D.F. value: Q≥200+10C		
		150℃ for	1hr then set for 24±2 hrs at room t	emp.	* I.R.: ≥1GΩ.		
14.	Humidity	* Test ten	np.: 40±2℃		* No remarkable damage.  * Cap change: within ±7.5% or ±0.75pF whichever is larger.  * Q/D.F. value: Q≥100+10/3C		
	(Damp Heat)	* Humidit	у: 90~95%RH				
	Load	* Test tim	e: 500+24/-0 hrs.				
		* To apply	To apply voltage: rated voltage		* I.R.: ≥500MΩ.		
		* Cap. / D	Cap. / DF(Q) / I.R. Measurement to be made after de-aging a		t		
		150℃ fo	r 1hr then set for 24±2 hrs at room	temp.			
15.	High	* Test ten	np.: 125±3℃	(石 /	* No remarkable damage.		
	Temperature	* To apply	y voltage: 200% of rated voltage.		* Cap change: within ±3.0% or ±0.3pF whichever is larger.		
	Load	* Test tim	e: 2000+24/-0 hrs.	估股份之	* Q/D.F. value: Q≥200+10C		
	(Endurance)	* Cap. / D	F(Q) / I.R. Measurement to be made	de after de-aging at	* I.R.: ≥1GΩ.		
		•	1hr then set for 24±2 hrs at room t		(F) (F)		
16.	ESR	The ESR	should be measured at room temp	erature and tested	V ( )		
		at freque	ncy 1±0.1 GHz.		0201	0402	
					0.05pF≤Cap≤1pF:< 350mΩ/pF	0.05pF≤Cap≤1pF:< 350mΩ/pF	
		PASSIVE SYSTEM ALL		1pF <cap≤5pf:< 300mω<="" th=""><th>1pF<cap≤5pf:< 300mω<="" th=""></cap≤5pf:<></th></cap≤5pf:<>	1pF <cap≤5pf:< 300mω<="" th=""></cap≤5pf:<>		
				5pF <cap≤22pf:< 250mω<="" th=""><th>5pF<cap≤100pf:< 250mω<="" th=""></cap≤100pf:<></th></cap≤22pf:<>	5pF <cap≤100pf:< 250mω<="" th=""></cap≤100pf:<>		
			1/1/2	<i>h</i> (	011,010		

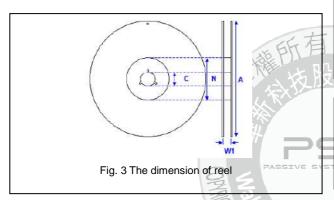
 $<sup>^{\</sup>star}$  "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

#### **APPENDIXES**

#### **■ Tape & reel dimensions**

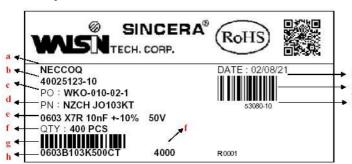


Size	0201	0402
Thickness	L	N
A <sub>0</sub>	0.40 +/-0.10	0.70 +/-0.20
B <sub>0</sub>	0.70 +/-0.10	1.20 +/-0.20
Т	≦0.55	≦0.80
K <sub>0</sub>	-	-
W	8.00 +/-0.30	8.00 +/-0.30
P <sub>0</sub>	4.00 +/-0.10	4.00 +/-0.10
10xP <sub>0</sub>	40.00 +/-0.10	40.00 +/-0.10
P <sub>1</sub>	2.00 +/-0.05	2.00 +/-0.05
P <sub>2</sub>	2.00 +/-0.05	2.00 +/-0.05
D <sub>0</sub>	1.50 +0.1/-0	1.50 +0.1/-0
D <sub>1</sub>	-	-
12E	1.75 +/-0.10	1.75 +/-0.10
(A) × F	3.50 +/-0.05	3.50 +/-0.05
四月瓜石		



Size	0201, 0402		
Reel size	7"	13"	
C	13.0±0.5	13.0±0.5	
W <sub>1</sub>	10.0±1.5	10.0±1.5	
EM AIA.IANC	178.0±2.0	330.0±2.0	
N	60.0+1.0/-0	50 min	

#### **■** Example of customer label



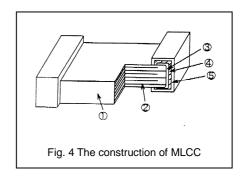
<sup>\*</sup>Customized label is available upon request

- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label

#### **Multilayer Ceramic Capacitors**

#### Constructions

No.	Na	NP0	
1)	Ceramic	Hi-Q dielectric ceramic	
2	Inner el	Cu	
3		Inner layer	Cu
4	Termination	Middle layer	Ni
(5)		Outer layer	Sn (Matt)



#### **■** Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70%. related humidity conditions; MSL Level 1.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

#### Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

## PASSIVE SYSTEM ALLIANCE

#### Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N<sub>2</sub> within oven are recommended.

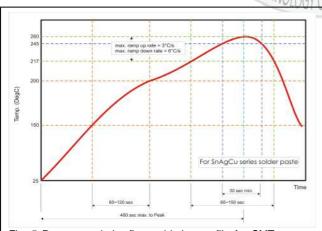


Fig. 5 Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.